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<u>L22</u>	L18 and 17	12	<u>L22</u>
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<u>L20</u>	L18 and 15	2	<u>L20</u>
<u>L19</u>	L18 and 14	28	<u>L19</u>
<u>L18</u>	L17 and balanc\$3	359	<u>L18</u>
<u>L17</u>	12 near55 (concurrent\$3 or simultaneous\$3 or parallel\$6)	1508	<u>L17</u>
<u>L16</u>	18 and 17	98	<u>L16</u>
<u>L15</u>	18 and 16	200	<u>L15</u>
<u>L14</u>	18 and 15	2	<u>L14</u>
<u>L13</u>	18 and 14	125	<u>L13</u>
<u>L12</u>	13 and 17	123	<u>L12</u>

<u>L11</u> 13 and 16	245	<u>L11</u>
<u>L10</u> 13 and 15	2	<u>L10</u>
<u>L9</u> 13 and 14	129	<u>L9</u>
<u>L8</u> (concurrent\$3 or simultaneous\$3 or parallel\$6) and 13 <i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>	3413	<u>L8</u>
<u>L7</u> (718/105)![CCLS]	832	<u>L7</u>
<u>L6</u> (718/102-108)![CCLS]	4123	<u>L6</u>
<u>L5</u> (712/31, 34,35,222)[CCLS]	88	<u>L5</u>
<u>L4</u> (712/2-300)[CCLS] <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	12394	<u>L4</u>
<u>L3</u> L2 and balanc\$3	4204	<u>L3</u>
<u>L2</u> L1 near35 (bus\$5 or frequen\$5 or overload\$3 or heav\$5 or occup\$5 or preoccup\$6 or "more" or "less" or "least" or "most")	19620	<u>L2</u>
<u>L1</u> (load\$1 or work near1 load\$1 or task\$1 or process\$2) near12 (assign\$7 or allocat\$6 or distribut\$5 or shar\$5) near15 (dual or plur\$7 or multipl\$7 or multi\$7 or second or two)	77393	<u>L1</u>

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» Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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**7. Multi-chip modules for analog and microwave: DC to 18 GHz**

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